

Partner: AudioScience

Models: Iyo Dante 8.8M, 16.16M, 32.32M, 16.0M, 32.0M, 0.16L 0.32L, 8.8MD, 16.16MD, 32.32MD, 16.0MD, 32.0MD, 0.16LD, 0.32LD, 8.8MR,

16.16MR, 32.32MR, 16.0MR, 32.0MR, 0.16LR, 0.32LR



| GENERAL INFORMATION | | | | |
|---|--|--|--|--|
| AudioScience Iyo Dante Command Processor IP v1.0 | | | | |
| Audio Processing | | | | |
| 1.0 | | | | |
| This module controls IP communication with an IYO Dante Interface. | | | | |
| This module is used in conjunction with the AudioScience IYO Dante Input Control module and AudioScience IYO Dante Output control module to control and monitor audio inputs and outputs. | | | | |
| Supports the following Products | | | | |
| lyo Dante 8.8M | | | | |
| lyo Dante 16.16M | | | | |
| lyo Dante 32.32M | | | | |
| lyo Dante 16.0M | | | | |
| lyo Dante 32.0M | | | | |
| lyo Dante 0.16L | | | | |
| lyo Dante 0.32L | | | | |
| lyo Dante 8.8MD | | | | |
| lyo Dante 16.16MD | | | | |
| lyo Dante 32.32MD | | | | |
| lyo Dante 16.0MD | | | | |
| lyo Dante 32.0MD | | | | |
| Iyo Dante 0.16LD | | | | |
| lyo Dante 0.32LD | | | | |
| lyo Dante 8.8MR | | | | |
| lyo Dante 16.16MR | | | | |
| lyo Dante 32.32MR | | | | |
| lyo Dante 16.0MR | | | | |
| lyo Dante 32.0MR | | | | |
| lyo Dante 0.16LR | | | | |
| lyo Dante 0.32LR | | | | |
| Crestron 3-Series processor. | | | | |
| N/A | | | | |
| N/A | | | | |
| | | | | |



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16.16MD, 32.32MD, 16.0MD, 32.0MD, 0.16LD, 0.32LD, 8.8MR,

16.16MR, 32.32MR, 16.0MR, 32.0MR, 0.16LR, 0.32LR

Device Type: Audio Processing



VENDOR SETUP: N/A



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16.16MD, 32.32MD, 16.0MD, 32.0MD, 0.16LD, 0.32LD, 8.8MR,

16.16MR, 32.32MR, 16.0MR, 32.0MR, 0.16LR, 0.32LR



| PARAMETERS: | |
|----------------------|--|
| Command_Processor_ID | The unique identifier of the Comm IP module for controlling the device specified by the IP address. If more than one hardware device is used in the solution design, one Comm module will need to be added with different Command Processor IDs for each device. |
| IP_Address | Setting to indicate the IP address for device communication. |



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16.16MR, 32.32MR, 16.0MR, 32.0MR, 0.16LR, 0.32LR



| CONTROL: | | |
|-----------------------------|---|--|
| Connect | D | Pulse to establish communication with the device and start the module "heartbeat" which is used to maintain communication with the device by periodically sending ping requests to confirm the device is still communicating with the control system. |
| Disconnect | D | Pulse to break communication with the device and stop the module "heartbeat". |
| Debug | D | Latch high to enable the internal trace messages printed in SIMPL Debugger. These messages may be useful while debugging to see what processes are occurring within the module. Note it is highly recommended to leave debugging disabled unless actively debugging as it causes much additional signal traffic in Debugger. |
| LED_Brightness_Set | D | Pulse to send the analog value of the target LED brightness to the device. The value sent is specified in the analog signal LED_Brightness_Target_Value. |
| LED_Brightness_Target_Value | Α | Value indicates the percentage of total device LED brightness. The range of valid values is 0 to 100. |



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16.16MR, 32.32MR, 16.0MR, 32.0MR, 0.16LR, 0.32LR



| FEEDBACK: | |
|------------------------------|--|
| Is_Communicating | D High indicates communication is established with the device. |
| Is_Initialized | D High indicates state variables of the device have been updated within the module. |
| LED_Brightness_Current_Value | A Value indicates the current LED brightness level as a percentage. The range of valid values is 0 to 100. |
| Model_Name_Text | S Text indicates the name of the device model. |
| Model_Number_Text | S Text indicates the model number of the device. |
| Serial_Number_Text | S Text indicates the serial number of the device. |
| Hardware_Revision_Text | S Text indicates the hardware revision of the device. |
| Firmware_Revision_Text | S Text indicates the firmware revision of the device. |
| MAC_Address_Text | S Text indicates the MAC address of the device. |



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16.16MR, 32.32MR, 16.0MR, 32.0MR, 0.16LR, 0.32LR

Device Type: Audio Processing



TESTING:

OPS USED FOR TESTING: CP3 1.600.3781.33119

SIMPL WINDOWS USED FOR TESTING: 4.11.06

CRES DB USED FOR TESTING: 79.00.003.00

DEVICE DATABASE: 105.07.001.00

SYMBOL LIBRARY USED FOR TESTING: 1089

SAMPLE PROGRAM: AudioScience Iyo Dante IP Demo.smw

REVISION HISTORY: v1.0 – Initial Release